

### Features

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses

### Typical Applications

- Inductive heating
- Electronic welders
- Self-commutated inverters

|                   |   |
|-------------------|---|
| $I_{T(AV)}$       | <b>800A</b>                             |
| $V_{DRM}/V_{RRM}$ | <b>1900 ~ 3000V</b>                     |
| $t_q$             | <b>30~100<math>\mu</math>s</b>          |
| $I_{TSM}$         | <b>8.5 kA</b>                           |
| $I^2t$            | <b>361 10<sup>3</sup>A<sup>2</sup>s</b> |

| SYMBOL                 | CHARACTERISTIC   | TEST CONDITIONS   |                        | $T_j(^{\circ}C)$ | VALUE |      |        | UNIT                             |
|------------------------|--|---|------------------------|------------------|-------|------|--------|----------------------------------|
|                        |  |   |                        |                  | Min   | Type | Max    |                                  |
| $I_{T(AV)}$            | Mean on-state current  | 180° half sine wave 50Hz<br>Double side cooled,   | $T_C=80^{\circ}C$      | 125              |       |      | 600    | A                                |
|                        |  |   | $T_C=55^{\circ}C$      |                  |       |      | 800    | A                                |
| $V_{DRM}$<br>$V_{RRM}$ | Repetitive peak off-state voltage<br>Repetitive peak reverse voltage | tp=10ms   |                        | 125              | 1900  |      | 3000   | V                                |
| $I_{DRM}$<br>$I_{RRM}$ | Repetitive peak current  | at $V_{DRM}$<br>at $V_{RRM}$  |                        | 125              |       |      | 60     | mA                               |
| $I_{TSM}$              | Surge on-state current   | 10ms half sine wave   |                        | 125              |       |      | 8.5    | kA                               |
| $I^2t$                 | $I^2t$ for fusing coordination                                       |   |                        |                  |       |      | 361    | A <sup>2</sup> s*10 <sup>3</sup> |
| $V_{TO}$               | Threshold voltage  |   |                        | 125              |       |      | 1.39   | V                                |
| $r_T$                  | On-state slope resistance  |   |                        |                  |       |      | 0.83   | m $\Omega$                       |
| $V_{TM}$               | Peak on-state voltage  | $I_{TM}=1800A, F=18kN$  | $30 \leq t_q \leq 55$  | 25               |       |      | 3.15   | V                                |
|                        |  |   | $56 \leq t_q \leq 75$  |                  |       |      | 2.88   | V                                |
|                        |  |   | $76 \leq t_q \leq 100$ |                  |       |      | 2.60   | V                                |
| dv/dt                  | Critical rate of rise of off-state voltage                           | $V_{DM}=0.67V_{DRM}$  |                        | 125              |       |      | 1000   | V/ $\mu$ s                       |
| di/dt                  | Critical rate of rise of on-state current                            | $V_{DM}=67\%V_{DRM}$ to 1600A,<br>Gate pulse $t_r \leq 0.5\mu s, I_{GM}=1.5A$<br>Single pulse |                        | 125              |       |      | 1200   | A/ $\mu$ s                       |
| $Q_{rr}$               | Recovery charge  | $I_{TM}=1000A, t_p=4000\mu s, di/dt=-20A/\mu s,$<br>$V_R=100V$                                |                        | 125              |       | 550  |        | $\mu C$                          |
| $t_q$                  | Circuit commutated turn-off time                                     | $I_{TM}=1000A, t_p=4000\mu s, V_R=100V$<br>dv/dt=30V/ $\mu$ s, di/dt=-20A/ $\mu$ s            |                        | 125              | 30    |      | 100    | $\mu s$                          |
| $I_{GT}$               | Gate trigger current   | $V_A=12V, I_A=1A$   |                        | 25               | 40    |      | 300    | mA                               |
| $V_{GT}$               | Gate trigger voltage   |   |                        |                  | 0.9   |      | 3.0    | V                                |
| $I_H$                  | Holding current  |   |                        |                  | 20    |      | 400    | mA                               |
| $I_L$                  | Latching current   |   |                        |                  |       |      | 500    | mA                               |
| $V_{GD}$               | Non-trigger gate voltage   | $V_{DM}=67\%V_{DRM}$  |                        | 125              |       |      | 0.3    | V                                |
| $R_{th(j-c)}$          | Thermal resistance<br>Junction to case                               | D.C. double side cooled<br>Clamping force 18kN  |                        |                  |       |      | 0.028  | $^{\circ}C/W$                    |
| $R_{th(c-h)}$          | Thermal resistance<br>case to heat sink                              |   |                        |                  |       |      | 0.0075 |                                  |
| $F_m$                  | Mounting force   |   |                        |                  | 15    |      | 20     | kN                               |
| $T_{vj}$               | Junction temperature   |   |                        |                  | -40   |      | 125    | $^{\circ}C$                      |
| $T_{stg}$              | Stored temperature   |   |                        |                  | -40   |      | 140    | $^{\circ}C$                      |
| $W_t$                  | Weight   |   |                        |                  |       | 320  |        | g                                |
| Outline                | P09  |   |                        |                  |       |      |        |                                  |

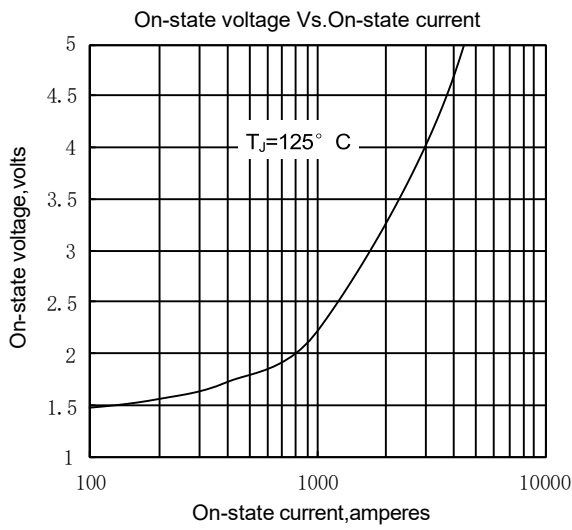


Fig.1

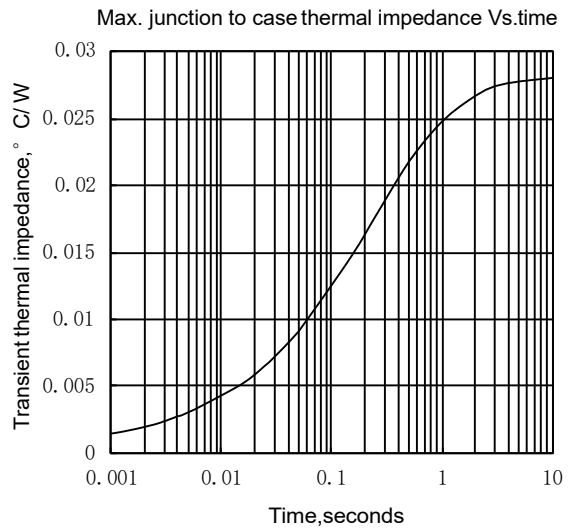


Fig.2

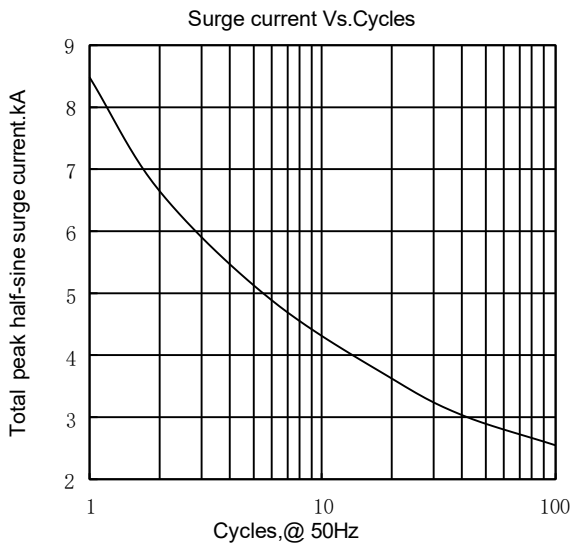


Fig.3

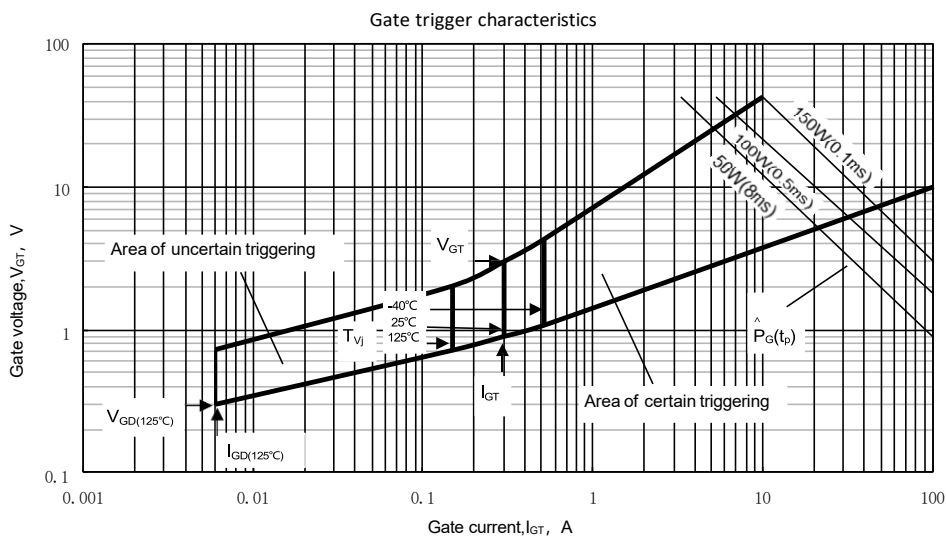
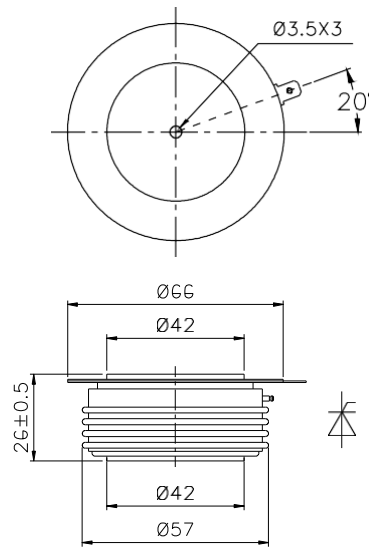


Fig.4

**Outline:**



Nlps reserves the right to change specifications without notice.