

**Features :**

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses
- Short turn-off time
- Hermetic metal cases with ceramic insulators

**I<sub>T(AV)</sub>**      **480A**  
**V<sub>DRM/V<sub>RRM</sub></sub>**      **1100~1400V**  
**t<sub>q</sub>**      **12~24μs**  
**I<sub>TSM</sub>**      **5.8kA**

**Typical Applications**

- Inductive heating
- Electronic welders
- Self-commutated inverters
- AC motor speed control
- General power switching applications



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T <sub>J</sub> (°C)	VALUE			UNIT
				Min	Type	Max	
I <sub>T(AV)</sub>	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T <sub>C</sub> =55°C	125		480	A
V <sub>DRM</sub> V <sub>RRM</sub>	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms	125	1100		1400	V
I <sub>DRM</sub> I <sub>RRM</sub>	Repetitive peak off-state current Repetitive peak reverse current	at V <sub>DRM</sub> at V <sub>RRM</sub>	125			30	mA
I <sub>T/f</sub>	High frequency on-state current	F=6KHz, T <sub>C</sub> =55°C				200	A
I <sub>TSM</sub>	Surge on-state current	10ms half sine wave V <sub>R</sub> =0.6V <sub>RRM</sub>	125			5.8	kA
I <sup>2</sup> t	I <sup>2</sup> t for fusing coordination					168	A <sup>2</sup> s*10 <sup>3</sup>
V <sub>TO</sub>	Threshold voltage		125			1.67	V
r <sub>T</sub>	On-state slope resistance					1.32	mΩ
V <sub>TM</sub>	Peak on-state voltage	I <sub>TM</sub> =600A, F=7.0kN	125			2.46	V
dv/dt	Critical rate of rise of off-state voltage	V <sub>DM</sub> =0.67V <sub>DRM</sub>	125			200	V/μs
di/dt	Critical rate of rise of on-state current	V <sub>DM</sub> = 67%V <sub>DRM</sub> to 800A Gate pulse t <sub>r</sub> ≤0.5μs I <sub>GM</sub> =1.5A	125			1500	A/μs
Q <sub>rr</sub>	Recovery charge	I <sub>TM</sub> =500A, tp=2000μs, di/dt=-60A/μs, V <sub>R</sub> =50V	125		38		μC
t <sub>q</sub>	Circuit commutated turn-off time	I <sub>TM</sub> =500A, tp=2000μs, V <sub>R</sub> =50V dv/dt=30V/μs ,di/dt=-60A/μs	125	12		24	μs
I <sub>GT</sub>	Gate trigger current	V <sub>A</sub> =12V, I <sub>A</sub> =1A	25	30		200	mA
V <sub>GT</sub>	Gate trigger voltage			0.8		2.5	V
I <sub>H</sub>	Holding current			20		250	mA
V <sub>GD</sub>	Non-trigger gate voltage	V <sub>DM</sub> =67%V <sub>DRM</sub>	125	0.3			V
R <sub>th(j-c)</sub>	Thermal resistance Junction to case	DC double side cooled Clamping force 7.0kN				0.045	°C/W
R <sub>th(c-h)</sub>	Thermal resistance case to heat sink					0.010	
F <sub>m</sub>	Mounting force			5.3		10	kN
T <sub>stg</sub>	Stored temperature			-40		140	°C
W <sub>t</sub>	Weight				80		g
Outline		P02					

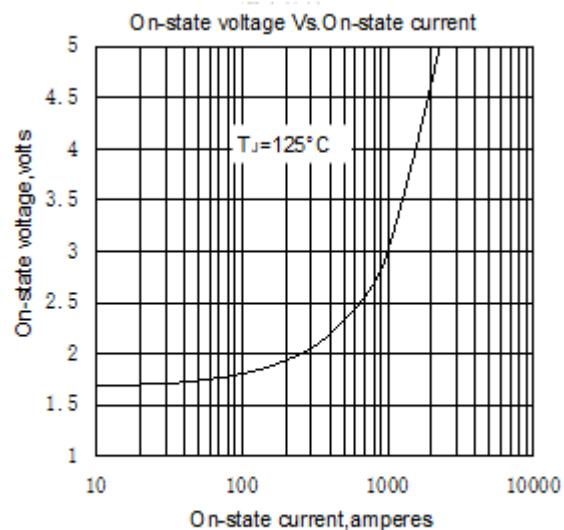


Fig. 1

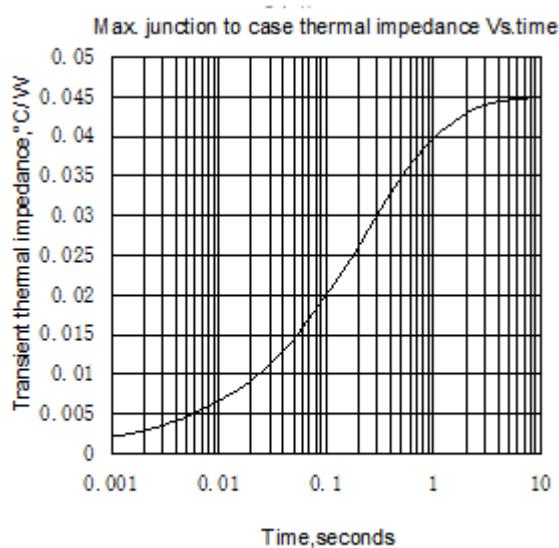


Fig. 2

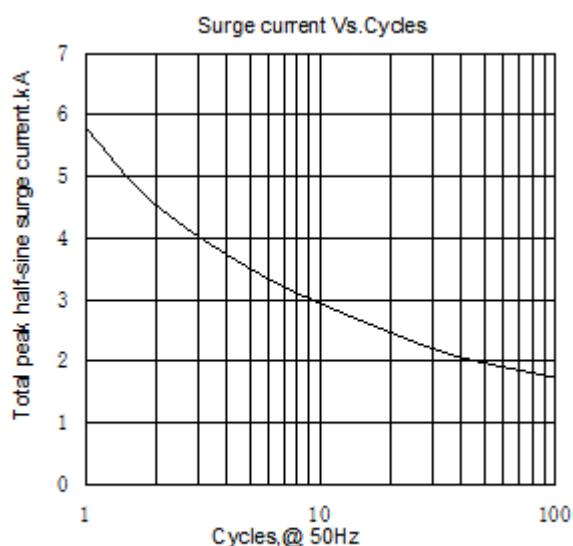


Fig. 3

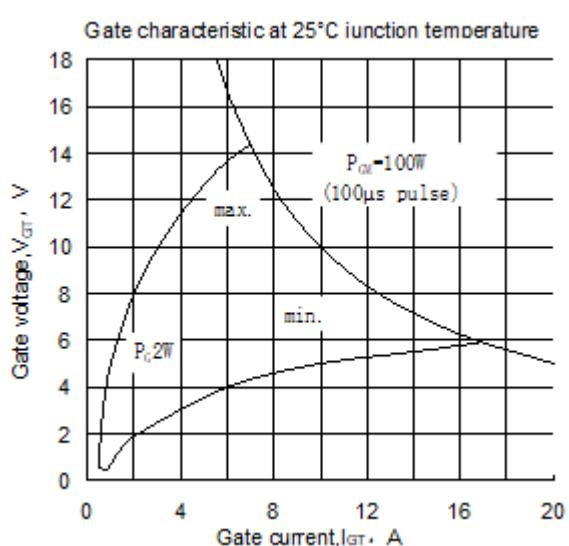


Fig. 4

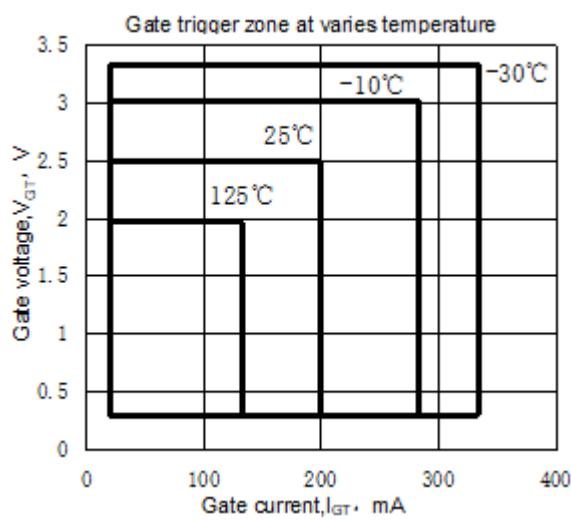


Fig. 5

