

Features

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses

Typical Applications

- Inductive heating
- Electronic welders
- Self-commutated inverters

$I_{T(AV)}$	2790A
V_{DRM}/V_{RRM}	1900 ~ 2500V
t_q	40~100μs
I_{TSM}	33.8 kA
I^2t	5712 10³A²S

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180□ half sine wave 50Hz Double side cooled, T _C =55°C	125			2790	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms	125	1900		2500	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			200	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			33.8	kA
I^2t	I^2t for fusing coordination	$V_R=0.6V_{RRM}$				5712	A ² s*10 ³
V_{TO}	Threshold voltage		125			1.48	V
r_T	On-state slope resistance					0.14	mΩ
V_{TM}	Peak on-state voltage	$I_{TM}=5000A, F=40kN$	25			3.15	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			1000	V/ μ s
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$, to3000A Gate pulse t _r ≤0.5 μ s I _{GM} =1.5A Single pulse	125			1200	A/ μ s
Q _{rr}	Recovery charge	$I_{TM}=2000A, tp=4000\mu s,$ $di/dt=-20A/\mu s, V_R=100V$	125		1460		μ C
t _q	Circuit commutated turn-off time	$I_{TM}=2000A, tp=4000\mu s, V_R=100V$ $dv/dt=30V/\mu s, di/dt=-20A/\mu s$	125	40		100	μ s
I _{GT}	Gate trigger current	$V_A=12V, I_A=1A$	25	40		450	mA
V _{GT}	Gate trigger voltage			0.9		4.5	V
I _H	Holding current			20		1000	mA
I _L	Latching current					1000	mA
V _{GD}	Non-trigger gate voltage			$V_{DM}=67\%V_{DRM}$	125		
R _{th(j-c)}	Thermal resistance Junction to case	D.C. double side cooled Clamping force 40kN				0.010	□C /W
R _{th(c-h)}	Thermal resistance case to heat sink					0.003	
F _m	Mounting force			35		47	kN
T _{vj}	Junction temperature			-40		125	□C
T _{stg}	Stored temperature			-40		140	□C
W _t	Weight				1100		g
Outline	P17						

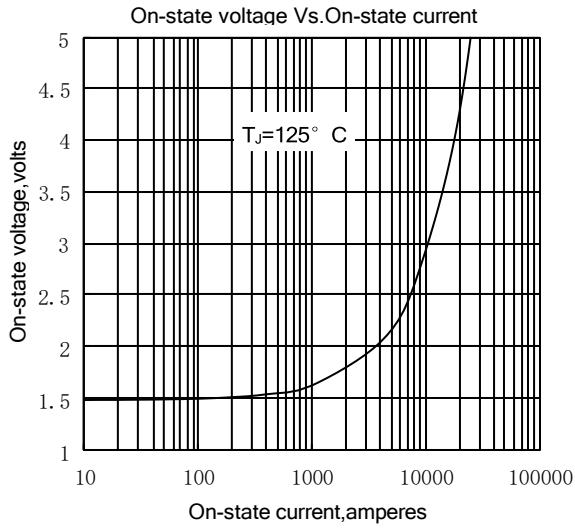


Fig.1

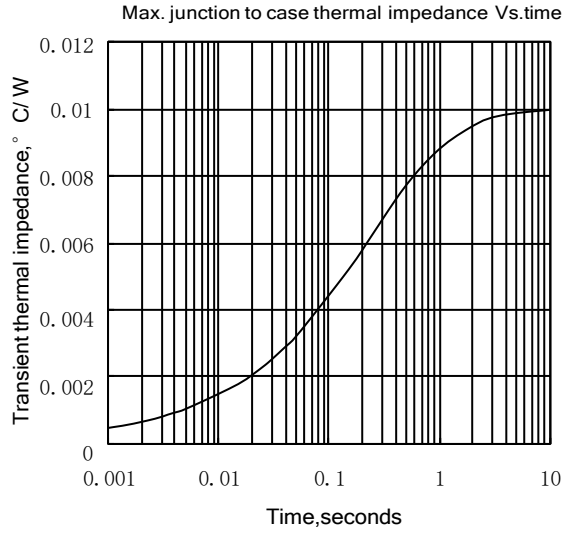


Fig.2

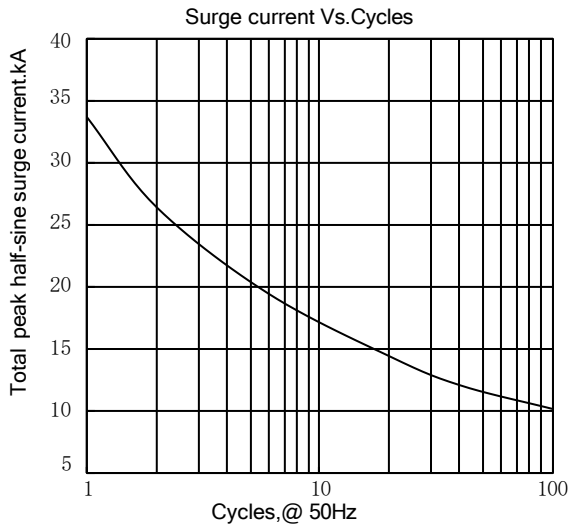


Fig.3

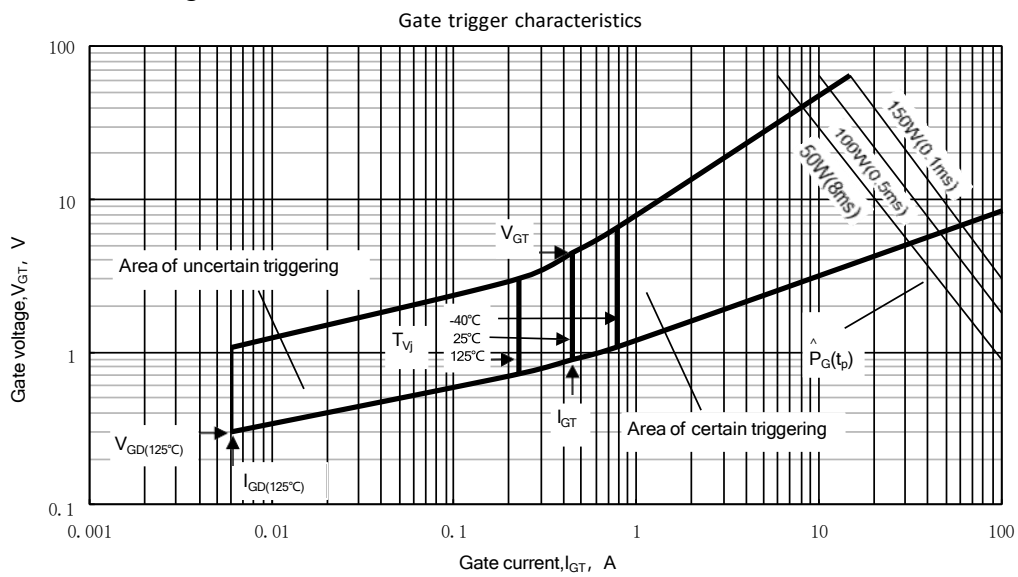
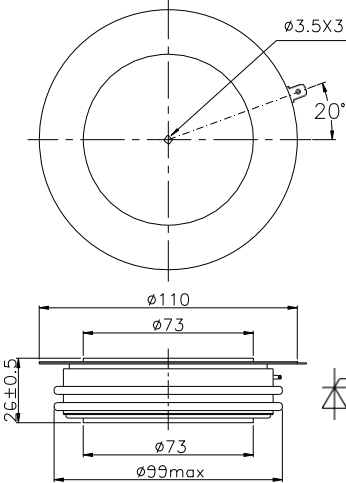


Fig.4

Outline:



Nlps reserves the right to change specifications without notice.