

**Features**

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses

**Typical Applications**

- Inductive heating
- Electronic welders
- Self-commutated inverters

品名 : FH2500TN**	
<b>I<sub>T(AV)</sub></b>	<b>2500A</b>
<b>V<sub>DRM</sub></b>	<b>800V~2000V</b>
<b>V<sub>RRM</sub></b>	<b>1000V~1800V</b>
<b>t<sub>q</sub></b>	<b>15~75μs</b>

SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T <sub>j</sub> (°C)	VALUE			UNIT
					Min	Type	Max	
I <sub>T(AV)</sub>	Mean on-state current	180° half sine wave 50Hz Double side cooled	T <sub>C</sub> =55°C	125			2500	A
V <sub>DRM</sub>	Repetitive peak off-state voltage	tp=10ms		125	800		2000	V
V <sub>RRM</sub>	Repetitive peak reverse voltage				1000		1800	V
I <sub>DRM</sub> I <sub>RRM</sub>	Repetitive peak current	at V <sub>DRM</sub> at V <sub>RRM</sub>		125			200	mA
I <sub>TSM</sub>	Surge on-state current	10ms half sine wave		125			29	kA
I <sup>2</sup> t	I <sup>2</sup> t for fusing coordination	V <sub>R</sub> =0.6V <sub>RRM</sub>					4205	10 <sup>3</sup> A <sup>2</sup> s
V <sub>TO</sub>	Threshold voltage			125			1.10	V
r <sub>T</sub>	On-state slope resistance						0.13	mΩ
V <sub>TM</sub>	Peak on-state voltage	I <sub>TM</sub> =4000A, F=32kN	15≤t <sub>q</sub> ≤28	25			2.20	V
			29≤t <sub>q</sub> ≤50				2.00	V
			51≤t <sub>q</sub> ≤75				1.80	V
dv/dt	Critical rate of rise of off-state voltage	V <sub>DM</sub> =0.67V <sub>DRM</sub>		125			1000	V/μs
di/dt	Critical rate of rise of on-state current (Non-repetitive)	V <sub>DM</sub> = 67%V <sub>DRM</sub> , Gate pulse t <sub>r</sub> ≤0.5μs I <sub>GM</sub> =1.5A		125			1500	A/μs
Q <sub>rr</sub>	Recovery charge	I <sub>TM</sub> =2000A, tp=4000μs, di/dt=-20A/μs, V <sub>R</sub> =100V		125		750		μC
t <sub>q</sub>	Circuit commutated turn-off time	I <sub>TM</sub> =2000A, tp=4000μs, V <sub>R</sub> =100V dv/dt=30V/μs, di/dt=-20A/μs		125	15		75	μs
I <sub>GT</sub>	Gate trigger current	V <sub>A</sub> =12V, I <sub>A</sub> =1A		25	40		250	mA
V <sub>GT</sub>	Gate trigger voltage				0.9		2.5	V
I <sub>H</sub>	Holding current				20		1000	mA
I <sub>L</sub>	Latching current						1000	mA
V <sub>GD</sub>	Non-trigger gate voltage	V <sub>DM</sub> =67%V <sub>DRM</sub>		125			0.3	V
R <sub>th(j-c)</sub>	Thermal resistance Junction to case	D.C. double side cooled Clamping force 32kN					0.012	°C/W
R <sub>th(c-h)</sub>	Thermal resistance case to heatsink						0.003	
F <sub>m</sub>	Mounting force				30		40	kN
T <sub>vj</sub>	Junction temperature				-40		125	°C
T <sub>stg</sub>	Stored temperature				-40		130	°C
W <sub>t</sub>	Weight					880		g
Outline	P14b							

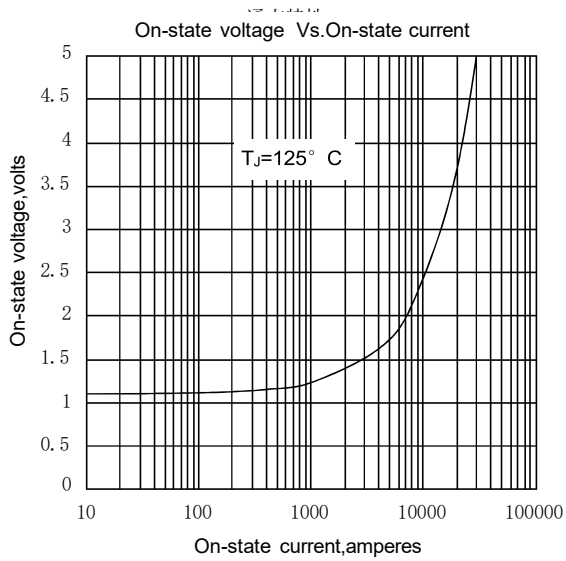


Fig.1

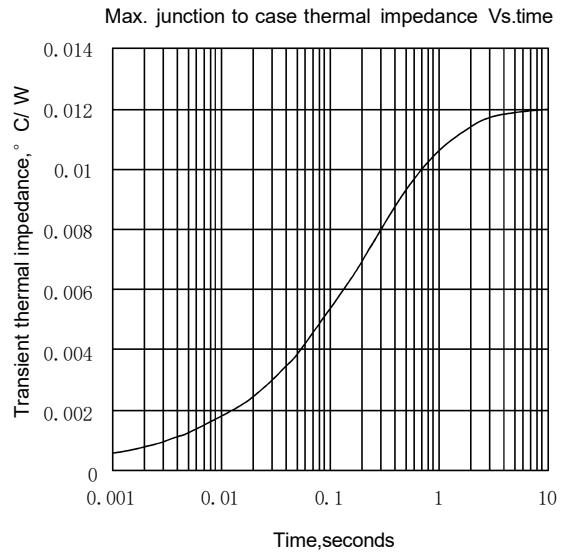


Fig.2

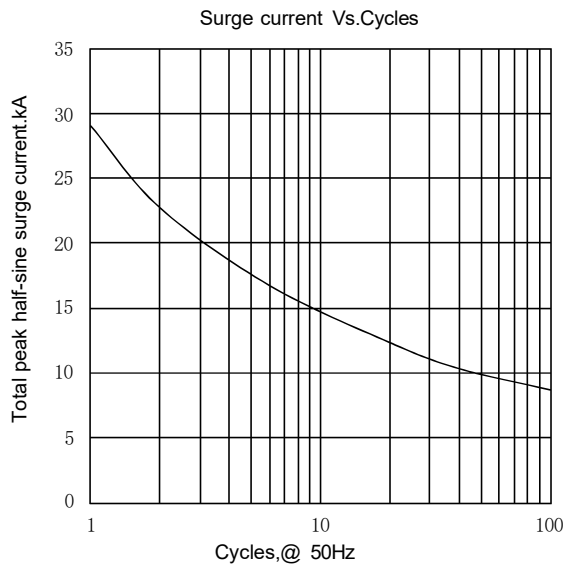


Fig.3

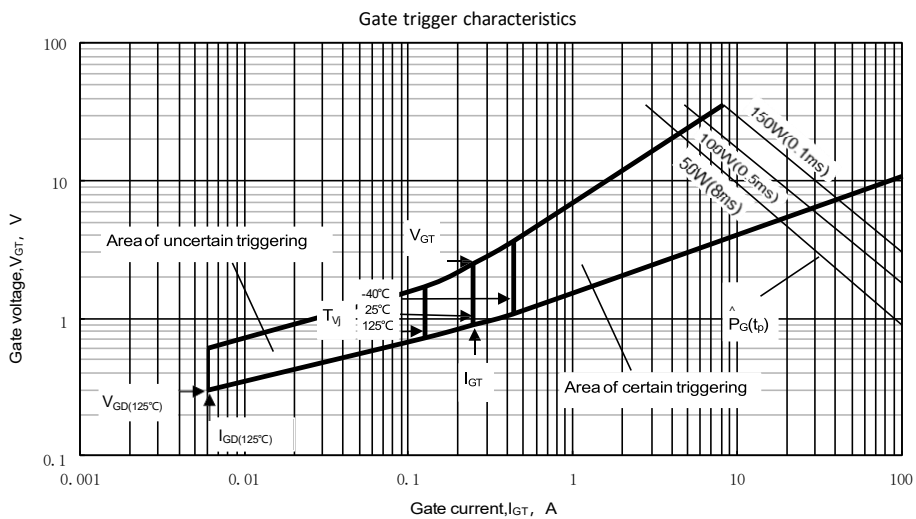
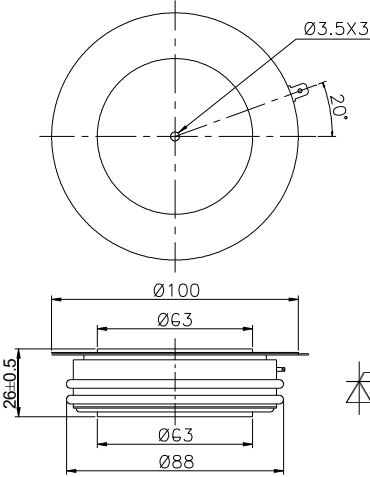


Fig.4

**Outline:**



Nlps reserves the right to change specifications without notice.