

**Features**

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

 **$I_{T(AV)}$  1050A** **$V_{DRM}/V_{RRM}$  3100~4200V****Typical Applications**

- AC controllers
- DC and AC motor control
- Controlled rectifiers

 **$I_{TSM}$  15 kA** **$I^2t$  1125 10<sup>3</sup>A<sup>2</sup>S**

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j(^{\circ}\text{C})$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	125			1050	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms	125	3100		4200	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$	125			100	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave	125			15	kA
$I^2t$	$I^2t$ for fusing coordination	$V_R=0.6V_{RRM}$				1125	A <sup>2</sup> s*10 <sup>3</sup>
$V_{TO}$	Threshold voltage		125			1.14	V
$r_T$	On-state slope resistance					0.57	mΩ
$V_{TM}$	Peak on-state voltage	$I_{TM}=1830\text{A}, F=24\text{kN}$	25			2.18	V
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			1000	V/μs
$di/dt$	Critical rate of rise of on-state current	$V_{DM}= 67\%V_{DRM}$ to 1300A, Gate pulse $t_r \leq 0.5\mu\text{s}$ $I_{GM}=1.5\text{A}$	125			100	A/μs
$Q_{rr}$	Recovery charge	$I_{TM}=2000\text{A}, tp=2000\mu\text{s}, di/dt=-20\text{A}/\mu\text{s},$ $V_R = 50\text{V}$	125		1800		μC
$I_{GT}$	Gate trigger current	$V_A=12\text{V}, I_A=1\text{A}$	25	40		300	mA
$V_{GT}$	Gate trigger voltage			0.8		3.0	V
$I_H$	Holding current			20		300	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125	0.3			V
$R_{th(j-c)}$	Thermal resistance Junction to case	DC double side cooled Clamping force 24kN				0.020	°C /W
$R_{th(c-h)}$	Thermal resistance case to heatsink					0.005	
$F_m$	Mounting force			19		26	kN
$T_{stg}$	Stored temperature			-40		140	°C
$W_t$	Weight				440		g
Outline		P11					

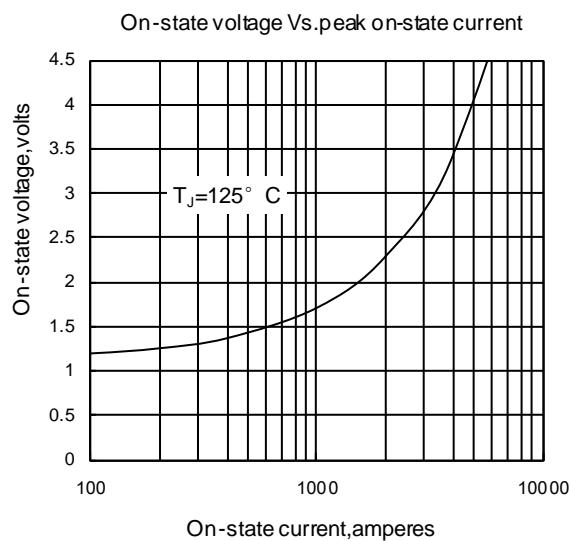


Fig1

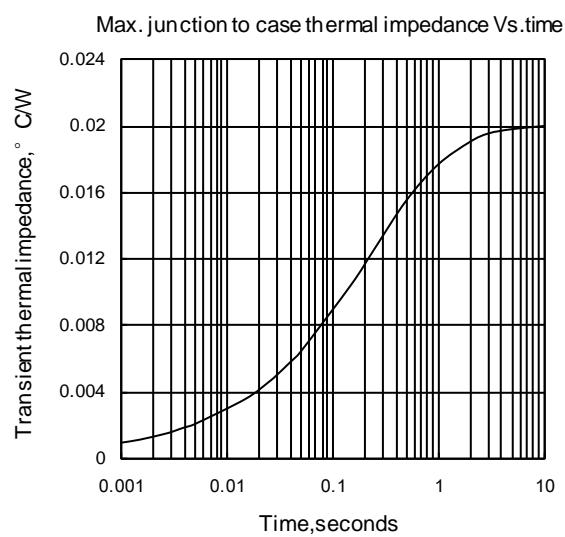


Fig2

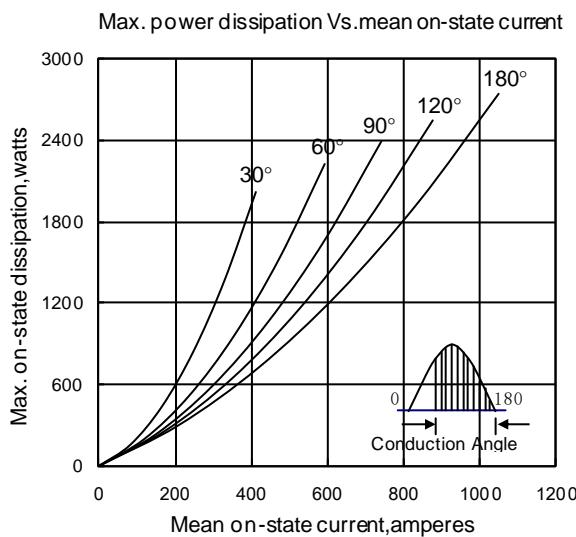


Fig3

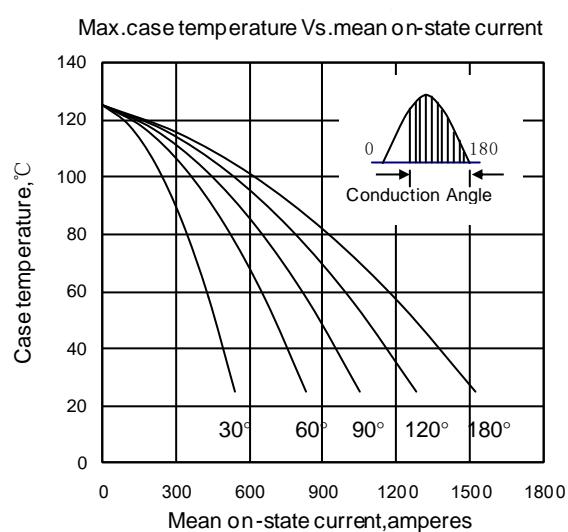


Fig4

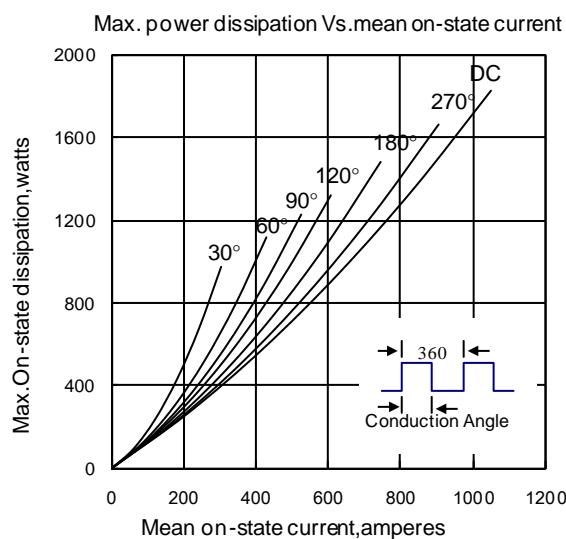


Fig5

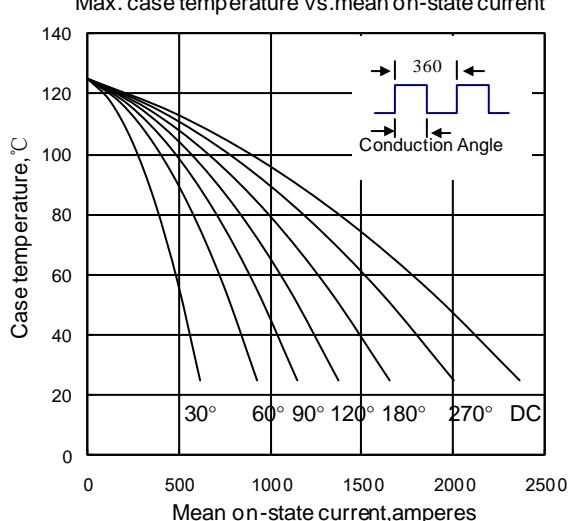


Fig6

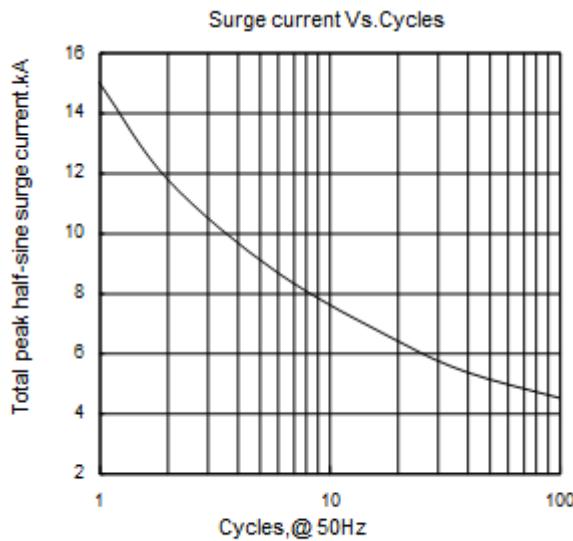


Fig7

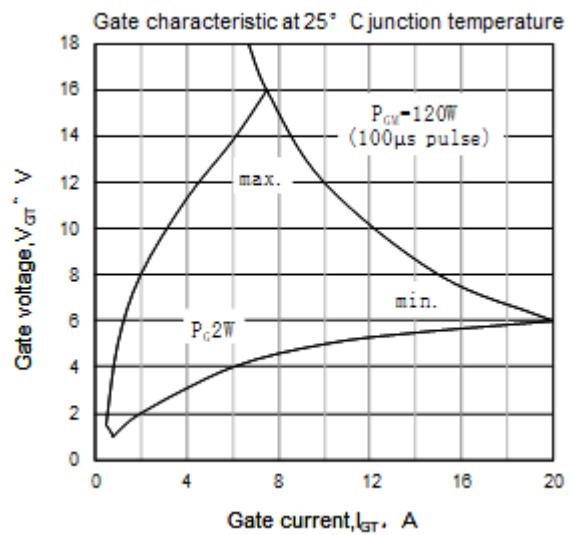


Fig8

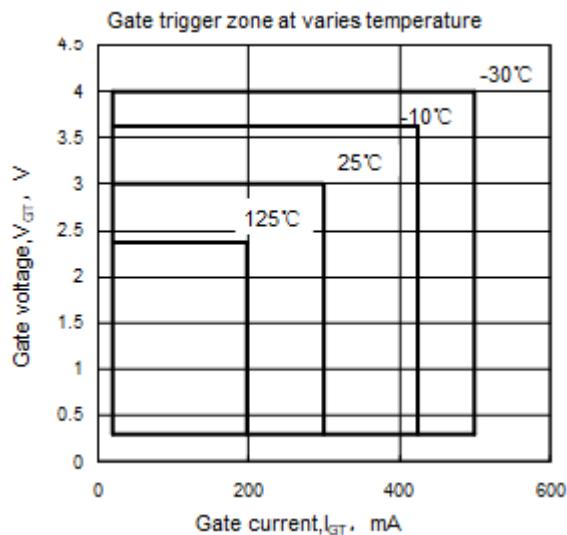


Fig9

