

Features :

- Isolated mounting base 3000V~
- Pressure contact technology with Increased power cycling capability
- Space and weight saving

Typical Applications :

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

V _{DSM} , V _{RSM}	V _{DRM} , V _{RRM}	品名
700V	600V	Mx500TH60
900V	800V	Mx500TH80
1100V	1000V	Mx500TH100
1300V	1200V	Mx500TH120
1500V	1400V	Mx500TH140
1700V	1600V	Mx500TH160
1900V	1800V	Mx500TH180

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Single side cooled, T _C =85°C	125			500	A
I _{T(RMS)}	RMS on-state current					785	A
I _{DRM} I _{RRM}	Repetitive peak current	at V _{DRM} at V _{RRM}	125			35	mA
I _{TSM}	Surge on-state current	V _R =60%V _{RRM} , t=10ms half sine,	125			14.5	kA
I ² t	I ² t for fusing coordination		125			1051	10 ³ A ² s
V _{TO}	Threshold voltage		125			0.80	V
r _T	On-state slope resistance					0.34	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =1500A	25			1.44	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =67%V _{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t _r ≤0.5μs Repetitive	125			200	A/μs
I _{GT}	Gate trigger current	V _A =12V, I _A =1A	25	30		200	mA
V _{GT}	Gate trigger voltage			0.8		3.0	V
I _H	Holding current			10		200	mA
I _L	Latching current					1000	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125			0.20	V
R _{th(j-c)}	Thermal resistance Junction to case	D.C. Single side cooled per chip				0.065	°C/W
R _{th(c-h)}	Thermal resistance case to heatsink	D.C. Single side cooled per chip				0.024	°C/W
V _{ISO}	Isolation voltage	50Hz, R.M.S, t=1min, I _{ISO} : 1mA(MAX)		3000			V
F _m	Terminal connection torque(M10)			10.0		12.0	N·m
	Mounting torque(M6)			4.5		6.0	N·m
T _{vj}	Junction temperature			-40		125	°C
T _{stg}	Stored temperature			-40		125	°C
W _t	Weight				1540		g
Outline	M06						

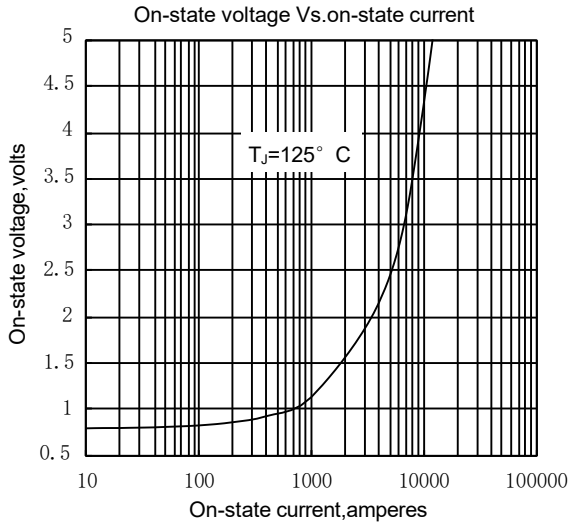


Fig.1

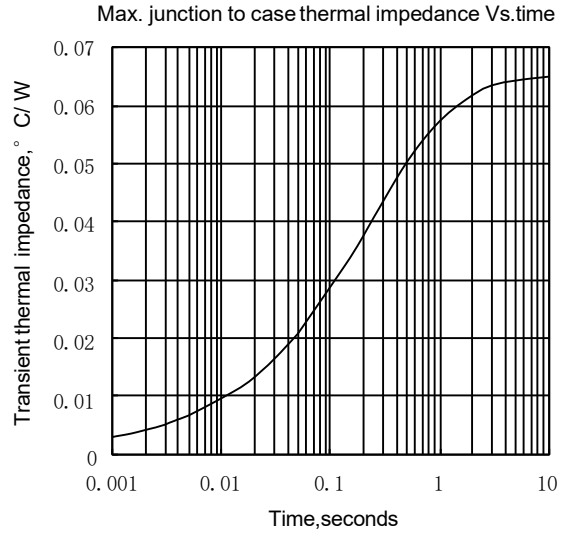


Fig.2

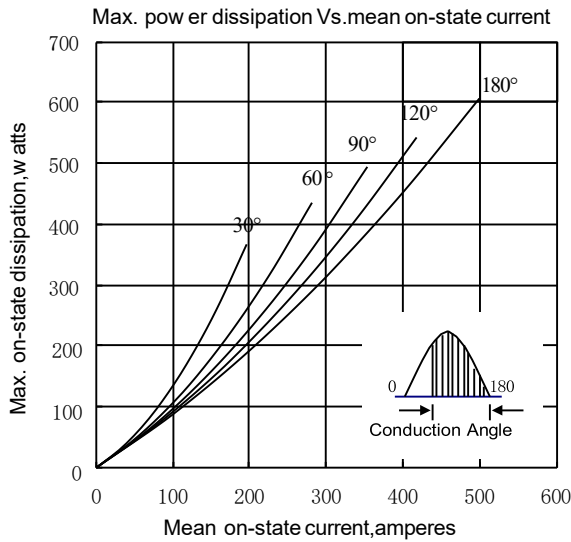


Fig.3

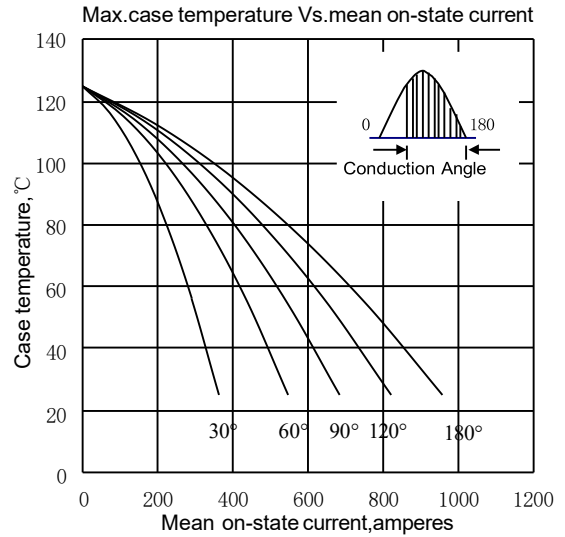


Fig.4

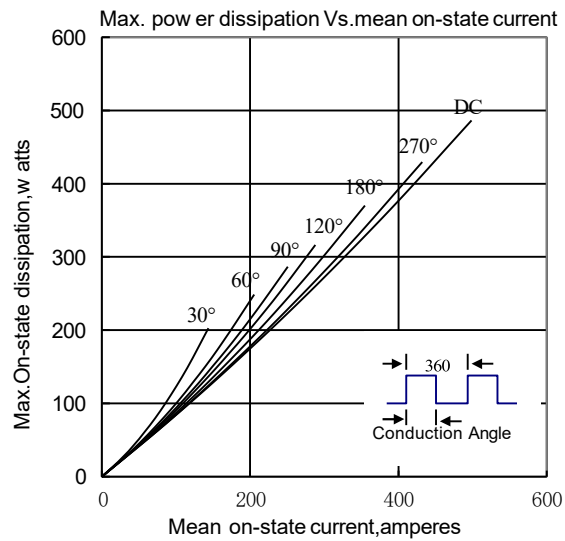


Fig.5

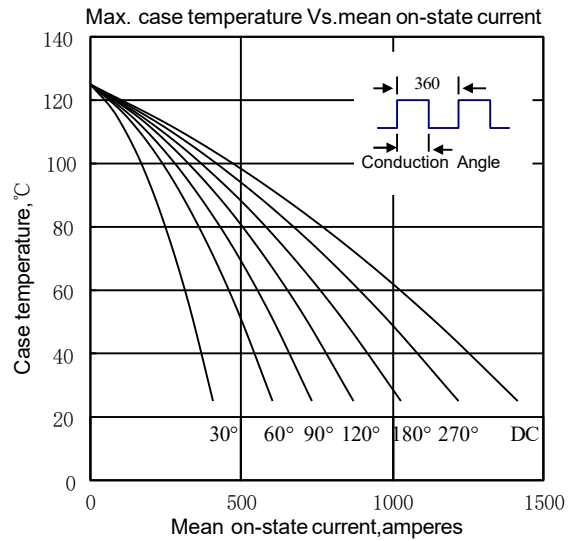


Fig.6

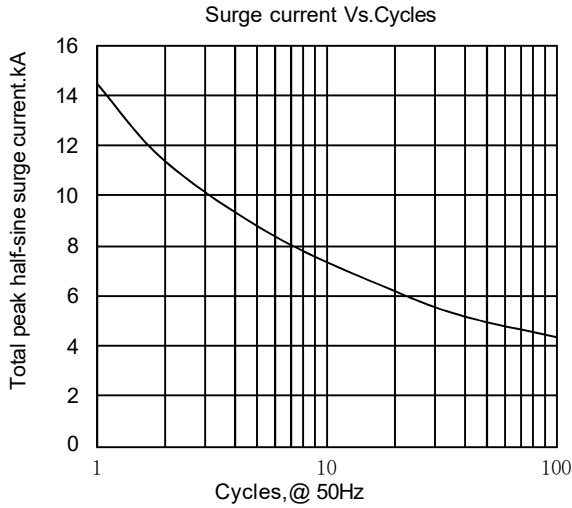


Fig7

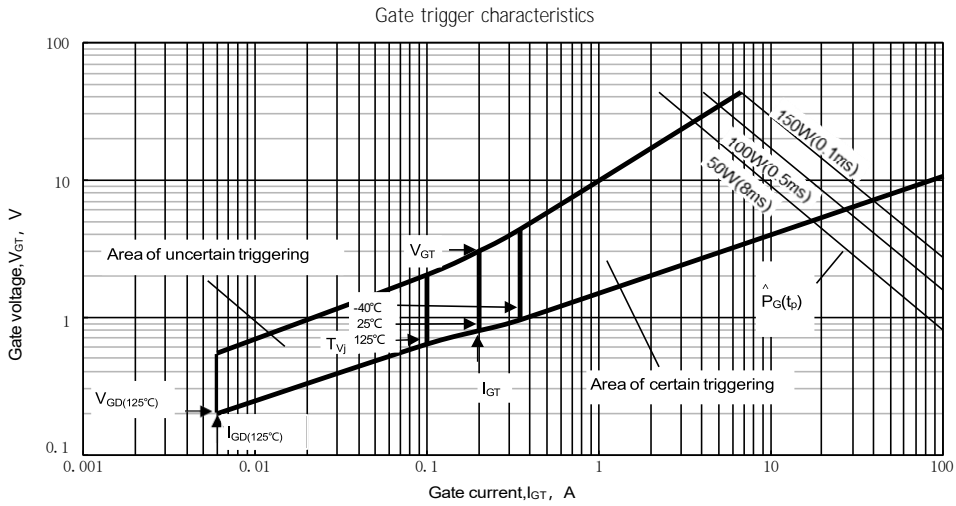
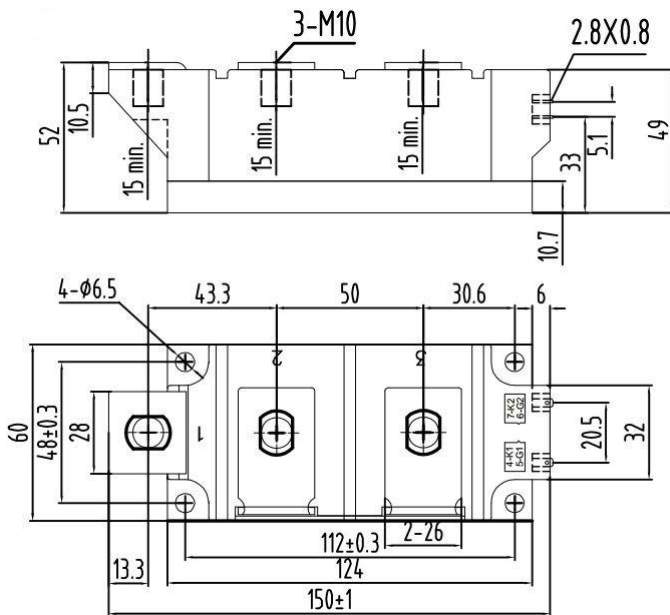


Fig. 8

Outline:



Unmarked dimensional tolerance: ± 0.5mm

