

Inrush Current Suppression Modules

Circuit configuration: Inrush current suppression circuit

ITEM	Part	V_{DRM}/V_{RRM}	I_o	I_{TSM}/I_{FSM}	I^2t	I_{DRM}/I_{RRM}	I_{GT}	V_{GT}	I_H	V_{FM}/I_{FM}	V_{TG}/V_{FO}	r_T/r_F	$R_{th(j-c)}$	$R_{th(c-h)}$	T_{jm}	Viso	Out Line No.
CONDITION		125°C	100°C	10ms		125°C	25°C			25°C	150°C	total					
UNIT		V	A	kA	kA ² s	mA	mA	V	mA	V/A	V	m·Ω	°C/W				
MG50TH**S	Thyristor	800-1,800	50	0.4	0.8	15	200	2.5	150	1.55/150	0.85	4.3	0.8	0.13	125	2,500	M32
	Diode	800-1,800	50	0.4	0.8	8	—	—	—	1.20/50	0.85	4.3	0.3	0.07	125		
MG75TH**S	Thyristor	800-1,800	75	0.5	1.25	15	200	2.5	250	1.60/230	0.85	3.1	0.4	0.13	125	2,500	M32
	Diode	800-1,800	75	0.5	1.25	8	—	—	—	1.25/75	0.85	3.1	0.25	0.08	125		
MG100TH**S	Thyristor	800-1,800	100	0.6	1.8	20	200	2.5	250	1.65/300	0.85	2.3	0.36	0.13	125	2,500	M32
	Diode	800-1,800	100	0.6	1.8	8	—	—	—	1.30/100	0.85	2.3	0.2	0.07	125		
MG150TH**S	Thyristor	800-1,800	150	1.3	8.45	40	200	2.5	250	1.65/450	0.85	1.6	0.2	0.10	125	2,500	M33
	Diode	800-1,800	150	1.3	8.45	8	—	—	—	1.40/150	0.85	1.6	0.15	0.07	125		
MG200TH**S	Thyristor	800-1,800	200	1.5	11.25	40	200	2.5	250	1.75/600	0.85	1.2	0.12	0.1	125	2,500	M33
	Diode	800-1,800	200	1.5	11.25	8	—	—	—	1.50/200	0.85	1.2	0.1	0.07	125		