

#### Features

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses

#### Typical Applications

- Inductive heating
- Electronic welders
- Self-commutated inverters

$I_{T(AV)}$	<b>2240A</b>
$V_{DRM}/V_{RRM}$	<b>800~1800V</b>
$t_q$	<b>30~60μs</b>
$I_{TSM}$	<b>28 kA</b>
$I^2t$	<b>3920 10<sup>3</sup>A<sup>2</sup>S</b>



SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T <sub>j</sub> (°C)	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T <sub>c</sub> =55°C	125			2240	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	tp=10ms		125	800		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$		125			160	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave		125			28	kA
$I^2t$	I <sup>2</sup> t for fusing coordination						3920	A <sup>2</sup> s*10 <sup>3</sup>
$V_{TO}$	Threshold voltage			125			1.45	V
$r_T$	On-state slope resistance						0.21	mΩ
$V_{TM}$	Peak on-state voltage	$I_{TM}=4000A, F=35kN$		125			2.29	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$		125			500	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 3000A Gate pulse $t_r \leq 0.5\mu s$ $I_{GM}=1.5A$		125			1200	A/μs
$Q_{rr}$	Recovery charge	$I_{TM}=2000A, tp=2000\mu s,$ $di/dt=-60A/\mu s, V_R=50V$		125		900		μC
t <sub>q</sub>	Circuit commutated turn-off time	$I_{TM}=2000A, tp=2000\mu s, V_R=50V$ $dv/dt=30V/\mu s, di/dt=-60A/\mu s$		125	30		60	μs
$I_{GT}$	Gate trigger current	$V_A=12V, I_A=1A$		25	40		450	mA
$V_{GT}$	Gate trigger voltage				0.9		4.5	V
$I_H$	Holding current				20		1000	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$		125	0.3			V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled					0.012	°C/W
$R_{th(c-h)}$	Thermal resistance case to heat sink	Clamping force 35kN					0.003	
$F_m$	Mounting force				30		40	kN
T <sub>stg</sub>	Stored temperature				-40		140	°C
$W_t$	Weight					880		g
Outline								

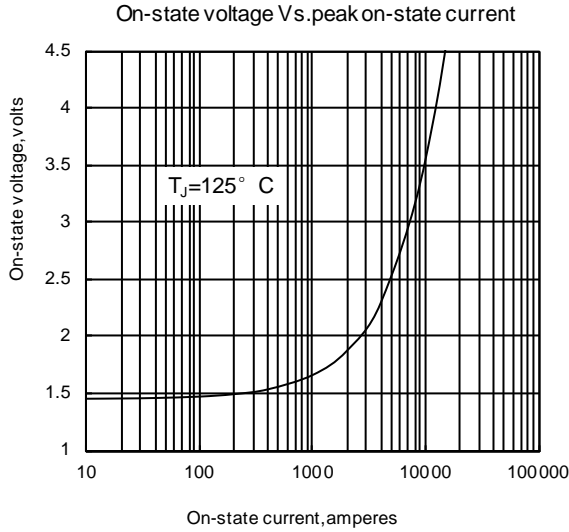


Fig1

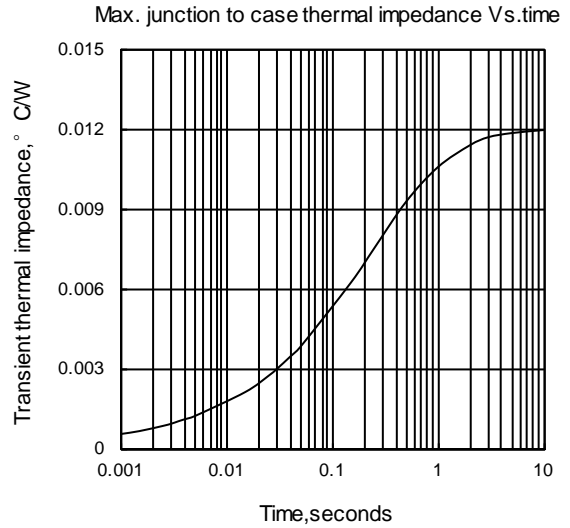


Fig2

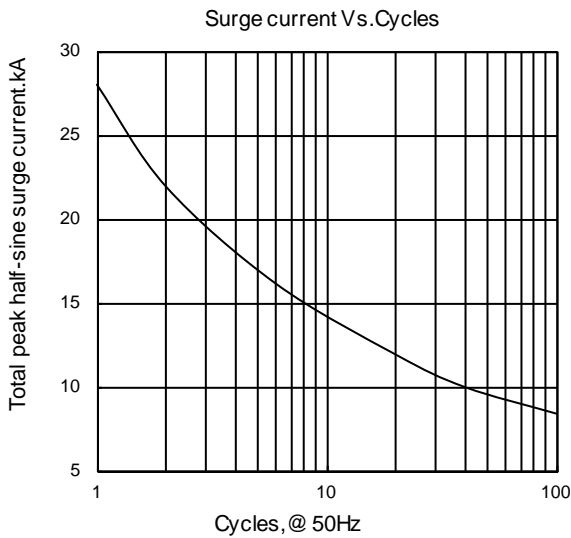


Fig3

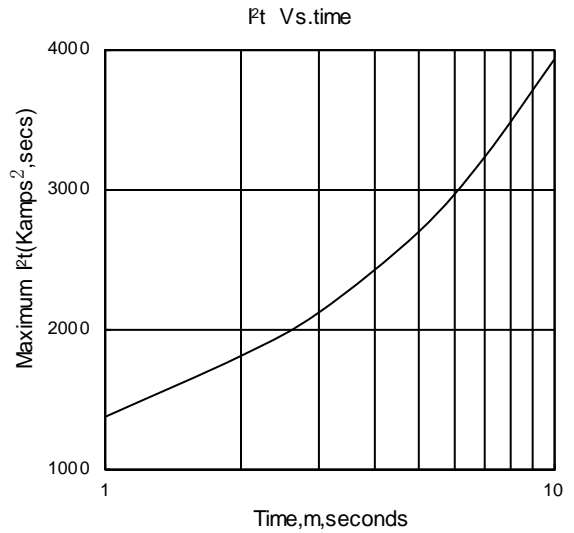


Fig4

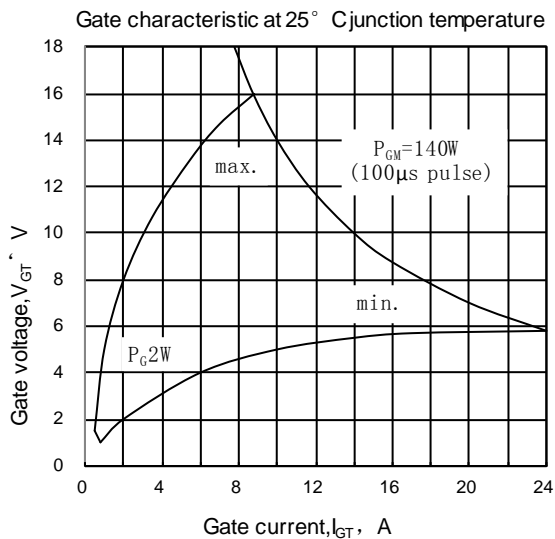


Fig5

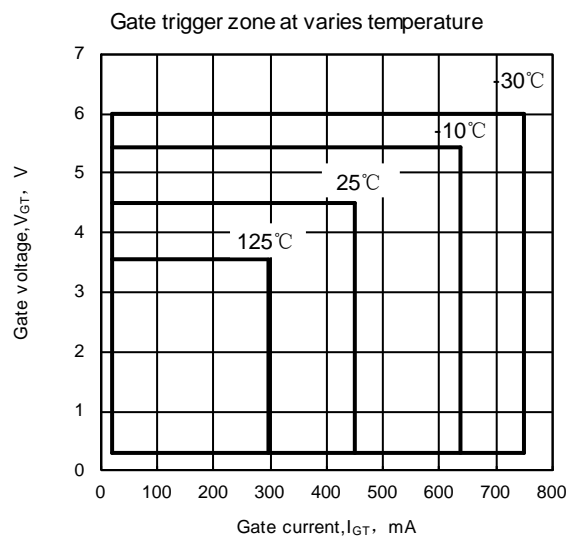


Fig6

**Outline:**

